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Attorney's Docket No.: 07937-107002 / US3194/3205/3215D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2812
Serial No.: 09/814,255 Examiner : Ori Nadav
Filed : March 21, 2001
Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

Total						
Claims	19	-	27	=	0	\$0
Independent	7	-	7	=	0	\$0

Applicant hereby petitions under 37 C.F.R.
\$1.136 for a 1 month extension of time. \$110

TOTAL FEE DUE \$110

A check for \$110 is attached.

Respectfully submitted,

Date: 5/29/02

James T. Hagler
Reg. No. 40,631

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL
I hereby certify under 37 CFR §1.8(a) that this
correspondence is being deposited with the
United States Postal Service as first class mail
with sufficient postage on the date indicated
below and is addressed to the Commissioner for
Patents, Washington, D.C. 20231.

5/29/2002

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Signature

Debbrah K. Sim

Typed or Printed Name of Person Signing
Certificate